

Listing of the Claims

1. (Previously Presented) A system comprising:

a bit stream comparison unit to compare a first bit value sequence of the received bit stream to a second bit value sequence associated with a transmitted bit stream to detect a sequence difference between the first and second bit value sequences; and

a multi-source agreement compliant electrical connector to convey the transmitted and received bit streams, wherein the multi-source agreement compliant electrical connector is coupled to the bit stream comparison unit, and wherein the multi-source agreement compliant electrical connector directly couples to a multi-source agreement compliant device.
2. (Original) The system of claim 1, wherein the bit stream comparison unit and the multi-source agreement compliant electrical connector are integrated on a circuit substrate.
3. (Original) The system of claim 2, wherein the circuit substrate is a printed circuit board.
4. (Original) The system of claim 1, wherein the multi-source agreement compliant device is an optical transceiver.

Claims 5-22 (Withdrawn)